Memory Product Specification

78.97G96.404
512MB 64MX72-Bit DDR2 533 Fully Buffered DIMM

Description

The Module is 64M x 72-bit (512MB) DDR2 533 CL4 SDRAM , Single rank FBD Memory module . The Module consists of nine 64MX8 bit DDR2 533 Synchronous DRAMs in FBGA packages, Memory Module intented for mounting into 240-pin edge connector sockets. The electrical and mechanical specifications are as follows

Features

- **JEDEC Standard**
- **DDR2 Speed Grade : 533Mbps**
- **FBD DIMM : 240-pin**
- **Memory Organization : Single rank of x8 FBGA DRAM chip**
- **DDR2 DRAM interface : SSTL_18**
- **CAS latency : 4-4-4**
- **Bandwidth : 4300MB/s**
- **VDD voltage : 1.8+-0.1V**
- **VDDQ voltage : 1.8+-0.1V**
- **Serial presence detect with EEPROM.**
- **PCB height : 1.195 inch**
- **RoHS Compliant**